

Docket Q00-1032-US1

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Adams et al

Serial No.: 09/659,481

Filed: 9/11/00

For: Coherent phase data
segment layout in data storage
system

) Group Art Unit

) Examining Attorney:

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JUL 11 2002

Technology Center 2600

INFORMATION DISCLOSURE STATEMENT

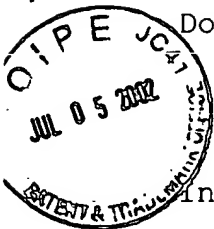
Honorable Commissioner of
Patents and Trademarks
Washington, D. C. 20231

Sir:

Attached hereto is one PTO-1449 form listing documents believed relevant to the subject application. It is respectfully requested that these documents be considered by the Examiner and an initialled copy of each form be returned to the undersigned.

It should be noted the word "prior" has been deleted from the form.

It is believed that this disclosure complies with the requirements of 37 C.F.R. 1.56 and the Manual of Patent Examining



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Procedures Section 707.05 (b). If for some reason the Examiner considers otherwise, it is respectfully requested that the undersigned be called so that any deficiencies can be remedied.

A copy of each document is enclosed.

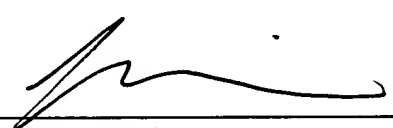
Some of the documents may have markings thereon. No significance is meant to be attached to the markings.

These documents are not necessarily analogous art.

Respectfully submitted,

Date: June 26, 2002

By


Michael Zarrabian
Reg. No. 39,886

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CERTIFICATE OF MAILING

I hereby certify that this correspondence or paper is being deposited with the United States Postal Service as first class mail in an envelope addressed to:
Commissioner of Patents, Washington, D.C. 20231, on June 26, 2002

By


Signature

Michael Zarrabian

Typed Name of Person Mailing Paper or Fee